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<b>UTILITY PATENT APPLICATION TRANSMITTAL</b> <small>(Only for new nonprovisional applications under 37 CFR 1.53(b))</small>	Attorney Docket No.	193130US2X
	First Inventor or Application Identifier	Naoya WADA, et al.
	Title	PHOTONIC NETWORK PACKET ROUTING METHOD AND PACKET ROUTER FOR PHOTONIC NETWORK

<b>APPLICATION ELEMENTS</b> <small>See MPEP chapter 600 concerning utility patent application contents</small>	<b>ADDRESS TO:</b> Assistant Commissioner for Patents Box Patent Application Washington, DC 20231
1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g. PTO/SB/17) <small>(Submit an original and a duplicate for fee processing)</small>  2. <input checked="" type="checkbox"/> Specification Total Pages <b>20</b>  3. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) Total Sheets <b>13</b> <b>(Formals)</b>  4. <input checked="" type="checkbox"/> Oath or Declaration Total Pages <b>4</b> a. <input checked="" type="checkbox"/> Newly executed (original) b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) <small>(for continuation/divisional with box 15 completed)</small> i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).  5. <input type="checkbox"/> Incorporation By Reference <small>(usable if box 4B is checked)</small> The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4B, is considered to be part of the disclosure of the accompanying application and is hereby incorporated by reference therein.	<b>ACCOMPANYING APPLICATION PARTS</b>  6. <input checked="" type="checkbox"/> Assignment Papers (cover sheet & document(s)) 7. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement <input type="checkbox"/> Power of Attorney <small>(when there is an assignee)</small> 8. <input type="checkbox"/> English Translation Document <small>(if applicable)</small> 9. <input checked="" type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input checked="" type="checkbox"/> Copies of IDS Citations (1) 10. <input type="checkbox"/> Preliminary Amendment 11. <input checked="" type="checkbox"/> White Advance Serial No. Postcard  12. <input type="checkbox"/> Small Entity Statement(s) <input type="checkbox"/> Statement filed in prior application. Status still proper and desired. 13. <input type="checkbox"/> Certified Copy of Priority Document(s) <small>(if foreign priority is claimed)</small> 14. <input checked="" type="checkbox"/> Other: Notice of Priority
15. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below: <input type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input type="checkbox"/> Continuation-in-part (CIP) of prior application no.: Prior application information: Examiner: Group Art Unit:	
16. Amend the specification by inserting before the first line the sentence: <input type="checkbox"/> This application is a <input type="checkbox"/> Continuation <input type="checkbox"/> Division <input type="checkbox"/> Continuation-in-part (CIP) of application Serial No. Filed on  <input type="checkbox"/> This application claims priority of provisional application Serial No. Filed	
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Title of the Invention:

Photonic network packet routing method and packet router for photonic network

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## BACKGROUND OF THE INVENTION

Field of the Invention:

This invention relates to a photonic network packet routing method and a packet router for a photonic network that are adapted for use in a photonic network utilizing optical fibers as transmission lines for transmitting information among many locations and that, at the time of transmitting packets labeled with destination addresses, distribute the packets onto appropriate optical paths at nodes for combining multiple optical pulses.

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Description of the Prior Art:

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Photonic network technologies for point-to-point transmission of information converted into optical signals have been applied to networks to create photonic networks. When, for example, the objective is to configure a network for transmitting Internet protocol (IP) packets over a photonic network, i.e., an IP-over-photonic network, photonic IP routers are required for switching the IP packets according to the IP addresses.

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FIG. 13 is a conceptual diagram of conventional photonic IP routing. The diagram shows how a photonic IP router processes an IP packet group consisting of first, second and third packets (Packets #1, #2 and #3) respectively labeled with IP addresses Addresses #2, #3 and #1. When Packet #1, Packet #2 and Packet #3 are input in time series through an optical signal input (IN) of the photonic IP router in the order mentioned, the photonic IP router switches Packet #1 labeled with IP Address #2 through a second output port (OUT 2), Packet #2 labeled with IP Address #3 through a third output port (OUT 3), and Packet #3 labeled with IP Address #1 through a first output port (OUT 1). In other words, the function of the photonic IP router is to read the addresses of the IP packets and switch each packet to the port appropriate for its address.

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FIG. 14 schematically illustrates the configuration of a photonic IP router 140 having N number of output ports. IP packets input through an optical input port (IN) are sent along two branches to both an address processing section 141 and a switching section 142.

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In the address processing section 141, a photodetector 141a converts each IP packet into an electric signal, a signal processor 141b reads its IP address and supplies the packet's routing information to a controller 141c. The controller 141c uses the

routing information to produce a control signal specifying the destination (one of the 1<sup>st</sup> to Nth ports) of the packet and sends the control signal to the switching section 142.

In the switching section 142, an optical delay unit 142a delays each received packet and then forwards it to a 1xN optical switch 142b for switching each input to one of the N number of output ports. When each IP packet is input to the 1xN optical switch 142b, the 1xN optical switch 142b is switched to the appropriate port by the control signal from the controller 141c of the address processing section 141. The IP packet is therefore output from the output port appropriate for the IP address of the IP packet.

Thanks to recent advances in photonic device technology, the optical switch 142b can have a switching speed faster than 1 ns (equivalent to 1 GHz). Switching speeds faster than 100 ps have been achieved in the laboratory.

Still, increasing the switching speed of the optical switch 142b does not increase the routing speed of the photonic IP router 140 as a whole because the processes between reading the IP address of the photoelectrically converted optical signal and control of the optical switch 142b by the controller 141c are conducted electrically. Since the arrival of the control signal from the controller 141c at the optical switch 142b is therefore delayed, each IP packet sent to the switching section 142 must therefore be delayed by the same amount by the optical delay unit 142a so that it will arrive at the 1xN optical switch 142b at the same time as the corresponding control signal. The routing speed is slowed in proportion to the delay.

The main object of this invention is to provide an ultra-fast packet routing method and a packet router capable of reading IP addresses from optical signals.

The present invention, accomplished for overcoming the foregoing problem, achieves high-speed routing by enabling all processing of IP packet addresses in the processing section (processing for selecting the course along which IP packets are to proceed) to be conducted optically rather than electrically, thereby eliminating a major roadblock to routing speed enhancement.

## SUMMARY OF THE INVENTION

This invention achieves this object by providing a photonic network packet routing method comprising a step of optically encoding destination address information attached to an IP packet using light attributes, a step of discriminating the encoded address information of the IP packet by optical correlation processing, a step of switching to an output path for the IP packet based on a result of the discrimination, and a step of outputting the IP packet labeled with prescribed address information on the

output path selected by the switching step.

The optically encoding of the destination address information attached to the IP packet can be conducted by dividing an optical pulse output by a pulse source into multiple chip pulses having a prescribed delay time therebetween, imparting the individual chip pulses with phase shifts of "0" or " $\pi$ " relative to a light carrier phase of the chip pulses, and recombining the divided optical chip pulses.

The optically encoding of the destination address information attached to the IP packet can be conducted by dividing an optical pulse into multiple chips having a prescribed delay time therebetween, changing normalized intensity of the individual chip pulses to "1" or "0", and recombining the divided optical chip pulses.

The discrimination of the encoded address information by optical correlation processing is conducted by subjecting the combined chip pulses to matched filtering, effecting threshold processing on a center peak value of a generated autocorrelation function, and optically regenerating the obtained "0" or "1".

The photonic network packet routing method can also comprise a step of dividing an IP packet having encoded address information in two, a step of sending one of the divided IP packets onto a number of arms equal to a number of address entries, a step of simultaneously conducting optical correlation processing on all arms in parallel to discriminate address information from an optical code in the packet containing the address information, selecting an IP packet output path based on a result of the discrimination, and outputting the other IP packet on the selected output path.

The optical code of one of the two divided IP packets is discriminated by optical correlation processing and the discriminated signal is used either in its unmodified form as an optical signal or after conversion to an electric signal, to open a gate of a prescribed output path.

This invention also provides a packet router for a photonic network comprising means for encoding by use of light attributes destination address information attached to an IP packet, branching means for sending the IP packet having the encoded destination address information onto two paths, address processing means for subjecting the one IP packet received thereby from the branching means to optical correlation processing to discriminate its address information and outputting a switch control signal based on a result of the discrimination, and switch means for selectively outputting the other packet by switching an output path of the other packet received from the branching means based on the address control signal from the address control means.

The means for attaching an optical code to the address information of the IP packet can be means comprising multiple tunable taps for dividing a light pulse output

by a pulse source into a prescribed number of optical chip pulses, optical phase shifters for imparting phase shifts of "0" or " $\pi$ " to each divided chip pulse, and a combiner for recombining the divided optical chip pulses.

The means for attaching an optical code to the address information of the packet can be means comprising multiple tunable taps for dividing a light pulse output by a pulse source into a prescribed number of optical chip pulses, gate switches for changing optical intensity of the chip pulses to "1" or "0", optical phases shifters for imparting phase shifts to the divided optical chip pulses, and a combiner for recombining the divided optical chip pulses.

The address processing means can be means comprising means for sending the one IP packet sent onto one path by the branching means onto a number of arms equal to the number of address entries, and decoders provided on the individual arms for outputting a switch control signal when the decoder's own code and the code of an IP packet coincide.

The switch means comprises means for sending the other IP packet sent onto the other path by the branching means onto a number of arms equal to the number of output ports and an optical gate provided on each arm that opens in response to a switching control signal from the decoder to output the IP packet onto the arm.

The packet router for a photonic network can comprise a combiner for combining an IP packet output through a prescribed path with a pulse signal for control adjusted to generate an optical pulse at a portion where it is desired to convert the optical code and a nonlinear optical medium for converting the combined signal into a prescribed optical code by cross-phase modulation.

Thus, in the present invention, destination address information attached to IP packets is optically encoded and the optical signal is read "as is" without conversion. Processing can therefore be conducted more rapidly by the conventional method in which address information is converted to electric signals for reading.

Faster processing is also achieved by sending the optical code of each packet including address information onto a number of arms equal to the number of address entries and simultaneously conducting optical correlation processing on all arms in parallel.

The above and other features of the present invention will become apparent from the following description made with reference to the drawings.

#### BRIEF EXPLANATION OF THE DRAWINGS

FIG. 1 is a schematic diagram showing the node of a photonic network

including a photonic IP router that is an embodiment of the present invention.

FIG. 2 is a block diagram schematically illustrating the configuration of a transmitter for transmitting IP packets labeled with IP addresses within the network of FIG. 1.

FIG. 3(a) is a time waveform characteristic diagram of the pulse train generated by the transmitter.

FIG. 3(b) is a frequency spectrum characteristic diagram of the signal generated by the transmitter.

FIG. 4 is a block diagram schematically illustrating the configuration of an optical bipolar encoder for encoding by "0", " $\pi$ " phase shift.

FIG. 5 is a diagram showing the characteristic of an 8-chip optical bipolar code generated by the optical bipolar encoder of FIG. 4.

FIG. 6 is a block diagram showing a photonic IP router that is a first embodiment of the invention.

FIG. 7(a) is a diagram showing the characteristic of a decoded signal waveform output by the optical encoder when the encoder's own code and the code of an input signal coincide.

FIG. 7(b) is a diagram showing the characteristic of a decoded signal waveform output by the optical encoder when the encoder's own code and the code of an input signal do not coincide.

FIG. 8 is block diagram schematically illustrating an optical unipolar encoder for encoding by "0", "1" pulse intensity.

FIG. 9 is a block diagram showing a photonic IP router that is a second embodiment of the invention

FIG. 10 is a block diagram showing a photonic IP router that is a third embodiment of the invention.

FIG. 11(a) is a block diagram showing an example of a converter for conversion of IP packet "0", " $\pi$ " optical codes.

FIG. 11(b) is a diagram illustrating the operating principle of optical code conversion conducted by the optical code converter of FIG. 11(a).

FIG. 12 is an explanatory diagram showing how an IP packet "0", "1" optical code is converted by the invention.

FIG. 13 is a schematic diagram of conventional photonic IP routing (three output ports).

FIG. 14 is a functional block diagram schematically illustrating a conventional photonic IP router.

## DESCRIPTION OF THE PREFERRED EMBODIMENTS

Embodiments of packet routers for photonic network utilizing the photonic network packet routing method of the present invention will now be explained with reference to the attached drawings.

FIG. 1 shows the basic architecture of a packet router for a photonic network configured for use in an IP-over-photonic network for transmitting Internet protocol (IP) packets. Designated by reference numeral 1 is a photonic IP router that receives IP packets 3... sent from a transmitter 2 over an optical fiber transmission line 4 and switches each for output over one of three transmission lines: a transmission line 5a (first output; OUT 1), a transmission line 5b (second output; OUT 2) and a transmission line 5c (third output; OUT 3). The photonic IP router 1 thus has 1x3 routing capability.

The transmitter 2 maps the address information of the each IP packet 3 in code (combination of 0,  $\pi$ ), labels the IP packet 3 with the mapped address information, and outputs the IP packet 3 on the optical fiber transmission line 4. For instance, the IP packet 3a is labeled with " $0\pi 0\pi 0\pi 0\pi$ " as second address information (Address #2), the IP packet 3b is labeled with " $0\pi 0\pi \pi 0\pi 0$ " as third address information (Address #3), and the IP packet 3c is labeled with " $0\pi \pi 000\pi 0$ " as first address information (Address #1).

The optical signal input to the photonic IP router 1 through the optical fiber transmission line 4 is sent along two branches to both an address processing section 6 and a switching section 7. In the address processing section 6, an optical processor 6a, which processes the optical signal without modification, reads the IP address information and outputs a signal for switching control. The signal for switching control produced by the optical processor 6a is forwarded to a photodetector 6b that converts it to a high-frequency electric signal that it supplies to the switching section 7 as a switching control signal. Since the address processing section 6 of the photonic IP router 1 conducts the reading of address information, reference to addresses for selecting a path and the output of the switching control signal optically, it enables a marked shortening of processing time.

The IP packets 3... sent to the switching section 7 are received by an optical delay unit 7a. The optical delay unit 7a imparts them with a time delay proportional to the difference in the lengths of the light paths of the address processing section 6 and the switching section 7 and then supplies them to an optical switch 7b. Based on the switching control signals received from the address processing section 6, the optical switch 7b switches each IP packet 3 to the output port appropriate for its IP address. For example, the IP packet 3c labeled with the first address information is switched to the

first output (transmission line 5a), the IP packet 3a labeled with the second address information is switched to the second output (transmission line 5b), and the IP packet 3b labeled with the third address information is switched to the third output (transmission line 5c).

5           The process for labeling the IP packets 3... with address information will now be explained in detail with reference to the detailed architecture of the transmitter 2 shown in FIG. 2.

10           A mode-locked laser (MLLD) 2b supplied with a signal from a 10GHz generator 2a serves as the coherent light source that produces a pulse train with a repetition frequency of 10GHz, a half value width of 2ps and a center frequency of  $\lambda$ . The time waveform of the pulse train is shown in FIG. 3(a) and its frequency spectrum characteristic in FIG. 3(b). This pulse chain is divided into two pulses, one for producing addresses and the other for producing data. One of the pulses is decelerated in its repetition rate up to the packet rate by a timing signal in an intensity modulator (EOM) 2c and supplied to an optical encoder 2d, where addresses are imparted thereto as optical codes in a completely optical manner.

15           The other pulse is modulated by an intensity modulator (EOM) 2e in accordance with packet data 3', combined with an optical code produced by the optical encoder 2d, amplified by an amplifier 2f, and output on the transmission line 4 as IP packets 3....

20           As an example, FIG. 4 schematically illustrates the architecture of an optical encoder 2d consisting of a PLC-type 8-chip optical bipolar encoder. The optical encoder 2d can be used as both an encoder and a decoder.

25           An optical pulse entering the optical encoder 2d is split by the action of tunable optical taps 41... and optical delay lines 42... connected to the tunable optical taps 41.... into two or more chips (eight chips in this embodiment) of equal intensity having a delay time between taps of 5ps. The optical carrier phases of the individual chip pulses are imparted with phase shifts of "0" or " $\pi$ " by an optical phase shifters 43.... The chip pulses are then combined by a combiner 44 to produce an 8-chip optical bipolar code. The combination of phase shifts imparted corresponds to one code. The desired optical bipolar code can thus be produced by controlling each optical phase shifter 43 in accordance with the address. FIG. 5 shows an example of a produced 8-chip optical bipolar code. Pulses of 2ps half value width separated by intervals of 5ps have carrier phases of 0,  $\pi$ , 0,  $\pi$ , 0,  $\pi$ , 0,  $\pi$ .

35           A first embodiment of the photonic IP router 1 that receives the IP packets labeled with address information in the foregoing manner is shown in FIG. 6. The IP packets input on the transmission line are sent along two branches to both the address



processing section 6 and the switching section 7.

The IP packets received by the address processing section 6 are further sent along a number of arms equal to the number of addresses (three in this embodiment). On the arms, the IP packets are simultaneously decoded by match-filtering in optical decoders 61a, 61b, 61c, and converted to electric signals by photodetectors 62a, 62b, 62c. As pointed out above, the optical decoders 61a, 61b, 61c can be configured identically to the PLC-type 8-chip optical bipolar encoder shown in FIG. 4. Configuring the optical decoders 61a, 61b, 61c as photonic processors enables the reading of address information and the production of the signal for switching control to be processed in a wholly optical manner.

Thus, as explained in the foregoing, the present invention conducts address processing by sending the address information-bearing optical code of each packet along a number of arms equal to the number of address entries and conducting optical correlation processing with respect to the optical codes on all arms simultaneously and in parallel. The processing can therefore be conducted rapidly.

FIG. 7 shows an example of the decoded signal waveform output by the optical decoders 61a-61c. When the decoder's own code and the code of the input signal coincide, the output waveform has a high peak (FIG. 7(a)). When the decoder's own code and the code of the input signal do not coincide, no high peak value is present in the output waveform (FIG. 7(b)). Therefore, an optical signal having a high peak is output by one of the optical decoders 61a-61c only when the code denoting address information of the input IP packet coincides with the optical decoder's own code. Threshold processing is conducted to discriminate whether the output optical signal meets the requirements of a switching control signal. Furthermore, the output of the optical decoders is subjected to time gate processing to cut off the center part and eliminate side-lobes of the correlation waveform. This can ensure the threshold processing for determining coincidence or noncoincidence of addresses. The time gate processing can be realized using a semiconductor gate switch device.

An IP packet received by the switching section 7 is sent to an optical delay unit 71 where it is imparted with a time delay proportional to the difference in the lengths of the light paths of the address processing section 6 and the switching section 7. The delayed IP packet is sent along three arms connected with optical gates 72a, 72b, 72c. The optical gates 72a-72c function as optical switches whose gates open only upon receiving a switching control signal from the address processing section 6 and stay closed at all other times. Each gate (ON-OFF switch) can, for example, be constituted as an LiNbO<sub>3</sub> intensity modulator. Here, the optical delay unit 71 serves as an optical

buffer memory that temporarily holds the packet in the optical region during the time of the address processing.

When the photonic IP router 1 configured in the foregoing manner receives the IP packet labeled with the first address information, only the optical decoder 61a outputs an optical signal having a high peak value and, as a result, only the photodetector 62a outputs a switching control signal. The optical gate 72a therefore opens to send the IP packet to the first output port. When the IP packet labeled with the second address information is received, only the optical decoder 61b outputs an optical signal having a high peak value and, as a result, only the photodetector 62b outputs a switching control signal. The optical gate 72b therefore opens to send the IP packet to the second output port. Similarly, when the IP packet labeled with the third address information is received, only the optical decoder 61c outputs an optical signal having a high peak value and, as a result, only the photodetector 62c outputs a switching control signal. The optical gate 72c therefore opens to send the IP packet to the third output port.

In the foregoing embodiment, encoding of the destination address information attached to the packet is conducted using "0" and " $\pi$ " phase shifts. Next, an embodiment will be described with reference to FIG. 8 in which the optical encoding is conducted using "0" and "1" based on pulse intensity.

A pulse entering an optical encoder 2d' is imparted with a prescribed time delay by an optical delay unit 42' connecting two tunable taps 41' and is divided into 8 chip pulses of equal intensity. The optical intensities of the 8 chip pulses are changed to "0" or "1" by an optical gate switch 45 and the chip pulses are then combined by a combiner 44' to produce an 8-chip optical intensity code. The imparted pulse intensity pattern corresponds to one code.

As in the embodiment using "0", " $\pi$ " encoding, when the IP packets having destination address information encoded with the aforesaid "0" and "1" are received by the address processing section, they are sent along two branches to both the address processing section and the switching section (see FIG. 6). In the address processing section, the optical codes of the IP packets including address information are sent along a number of arms equal to the number of address entries. On the arms, the optical signals are decoded by match-filtering in decoders. A signal having a high peak is output by the decoder only when the input optical code and the code of the optical decoder coincide. The optical decoder has the same configuration as the optical encoder.

An IP packet received by the switching section is sent to an optical delay unit where it is imparted with a time delay proportional to the difference in the lengths of the light paths of the address processing section and the switching section. The delayed IP

packet is sent along output port arms. Each arm has an optical gate connected with one of the decoders. The gate of the arm opens to output the IP packet only when input with a switching control signal from the decoder of the address processing section.

FIG. 9 is a block diagram showing a second embodiment of the photonic IP router. The photonic IP router 1' of FIG. 9 adopts the same means as the router of the first embodiment for decoding encoded address information of input IP packets and controlling the optical switches by switching control signals based on the decoded signals. As explained in the following, the photonic IP router 1' differs from that of the first embodiment in ability to suppress input optical signal loss. Constituent elements similar to those of the router of the first embodiment are assigned like reference symbols and will not be explained again here.

An IP packet received by the photonic IP router 1' is sent along two branches. The IP packet sent to a switching section 7' goes to an optical delay unit 71 where it is imparted with a time delay proportional to the difference in the lengths of the light paths of an address processing section 6' and the switching section 7'. The delayed IP packet is sent to a first optical switch 73a. When the address information of the IP packet is such that it should be sent to the first output port, the first optical switch 73a has received a switching control signal from a photodetector 62a of the address processing section 6' and the IP packet is output from an A port of a first optical switch 73a and sent to a first output port.

When the address information of the IP packet is not such that it should be sent to the first output port, the first optical switch 73a has not received a switching control signal from the photodetector 62a of the address processing section 6' and the IP packet is output from a B port of the first optical switch 73a and sent to a second switch 73b. When the address information of the IP packet is such that it should be sent to the second output port, the second optical switch 73b has received a switching control signal from a photodetector 62b of the address processing section 6' and the IP packet is output from an A port of a second optical switch 73b and sent to a second output port. The 1x2 optical switches (the first and first optical switches 73a, 73b) can be constituted as LiNbO<sub>3</sub> Mach-Zehnder type dual output intensity modulators.

When the address information of the IP packet is not such that it should be sent to either the first output port or the second output port, it is output from a B port of the second switch 73b and sent to a third output port.

The photonic IP router 1' of the second embodiment uses two-output switches (port changeover switches) as the switches of the switching section 7' and, therefore, unlike the photonic IP router 1 according to the first embodiment, does not constantly

waste two-thirds of the energy consumed by the switching section. The second embodiment thus provides a low-loss photonic IP router.

FIG. 10 is a block diagram showing a third embodiment of the photonic IP router. The photonic IP router 1'' of FIG. 10 adopts the same means as the routers of the first and second embodiments for decoding encoded address information of input IP packets and controlling the optical switches by switching control signals based on the decoded signals. As explained in the following, the photonic IP router 1'' differs from that of the first and second embodiments in its feature of effecting optical switch control without converting the optical signals to electric signals, thereby eliminating operational delay caused by using electric signals converted from optical signals for optical switch control. Constituent elements similar to those of the routers of the first and second embodiments are assigned like reference symbols and will not be explained again here.

An IP packet received by the photonic IP router 1'' is sent along two branches. The IP packet sent to a switching section 7'' passes through a first optical circulator 74a to a first nonlinear optical loop mirror (NOLM #1) 75a. The first nonlinear optical loop mirror 75a is composed of a first directional combiner 76, an optical fiber 77 formed into a loop, and a second directional combiner 78 located at an appropriate point on the optical fiber 77. Upon reaching the first directional combiner 76, the IP packet is divided into a component traveling clockwise in the optical fiber 77 and a component traveling counterclockwise therein. After making one rotation, the components are combined by the first directional combiner 76. The arrangement is predefined such that the IP packet combined by the first directional combiner 76 normally (when no control signal is applied to the second directional combiner 78) is output onto an arm on the right side (opposite from the input side) of the first nonlinear optical loop mirror 75a.

When an optical decoder 61a of an address processing section 6'' read the address information and output a signal for switching control, the signal for switching control, supplied to the switching section 7'' still in optical form, passes through a first optical delay unit 71a to the second directional combiner 78 of the first nonlinear optical loop mirror 75a. Then when a switching control signal is input through the second directional combiner 78 at proper timing relative to the light wave traveling clockwise in the optical fiber 77, the phase of the optical signal traveling clockwise is changed by cross-phase modulation. Owing to this phase change, the IP packet combined by the first directional combiner 76 is output on an arm on the left side (input side) of the first nonlinear optical loop mirror 75a.

Thus, the optical signal (IP packet) output from the first nonlinear optical loop mirror 75a is switched to the left or right depending on whether or not a switching

control signal from the address processing section 6'' is present at the first nonlinear optical loop mirror 75a. Therefore, when address information of an IP packet input to the photonic IP router 1'' is such that the IP packet should be sent to a first output port, the IP packet is returned from the first nonlinear optical loop mirror 75a to the first optical circulator 74a and is sent to the first output port (OUT 1) by the first optical circulator 74a.

When the address information of an IP packet is not such that the IP packet should be sent to the first output port, the IP packet is sent onto the arm on the right side (opposite from the input side) of the first nonlinear optical loop mirror 75a and then passes through a second circulator 74b to a second nonlinear optical mirror (NOLM #2) 75b. The second nonlinear optical mirror 75b is also composed of a first directional combiner 76, an optical fiber 77 formed into a loop, and a second directional combiner 78 located at an appropriate point on the optical fiber 77. Upon reaching the directional combiner 76, the IP packet is divided into a component traveling clockwise in the optical fiber 77 and a component traveling counterclockwise therein. After making one rotation, the components are combined by the first directional combiner 76. The arrangement is predefined such that the IP packet combined by the first directional combiner 76 normally (when no control signal is applied to the second directional combiner 78) is output onto an arm on the right side (opposite from the input side) of the second nonlinear optical loop mirror 75b.

When an optical decoder 61b of the address processing section 6'' read the address information and output a signal for switching control, the signal for switching control, supplied to the switching section 7'' still in optical form, passes through a second optical delay unit 71b to a second directional combiner 78 of the second nonlinear optical loop mirror 75b. Then when a switching control signal is input through the second directional combiner 78 at proper timing relative to the light wave traveling clockwise in the optical fiber 77, the phase of the optical signal traveling clockwise is changed by cross-phase modulation. Owing to this phase change, the IP packet combined by the first directional combiner 76 is output on an arm on the left side (input side) of the second nonlinear optical loop mirror 75b.

Thus, the optical signal (IP packet) output from the second nonlinear optical loop mirror 75b is switched to the left or right depending on whether or not a switching control signal from the address processing section 6'' is present at the second nonlinear optical loop mirror 75b. Therefore, when address information of an IP packet input to the photonic IP router 1'' is such that the IP packet should be sent to a second output port, the IP packet is returned from the second nonlinear optical loop mirror 75b to the

second optical circulator 74b and is sent to the second output port (OUT 2) by the second optical circulator 74b.

When the address information of an IP packet is not such that the IP packet should be sent to either the first output port or the second output port, the IP packet is sent through an arm on the right side (opposite from the input side) of the second nonlinear optical loop mirror 75b to a third output port.

Since the photonic IP router 1'' according to the third embodiment uses nonlinear optical loops (NOLMs) as a light-to-light 1x2 switch, switch control can be conducted directly by light, without converting the signals for switching control into electrical signals. A photonic IP router not dependent on electric circuitry likely to cause a bottleneck can therefore be realized.

The photonic IP routers 1, 1' and 1'' according to the first to third embodiments were all explained with regard to the case of generating IP addresses using pulse code. The present invention is, however, not limited to this method of generating the IP addresses attached to the IP packets. Generally, IP addresses can be encoded by optical signals that can discriminate between coincidence and noncoincidence by correlation processing in the optical region.

As explained in the foregoing, the switch section is controlled by switching control signals from the address processing section so as to switch every IP packet input thereto to the output port appropriate for its IP address, whereafter the IP packet is transmitted to the next network node as a prescribed optical pulse. In some cases the optical code of the IP packet may have to be converted and the header rewritten.

FIG. 11(a) is a block diagram showing an example of a converter for use in the case where the optical code of the IP packet is a "0", " $\pi$ " phase-shift code.

IP packets requiring code conversion output by an IP router 81 are sent to a combiner 82. A  $\lambda_c$  pulse signal different from the IP packet is sent from a pulse source 83 to an optical encoder 84 as control light. In the optical encoder 84, adjustment is effected with respect to the input pulse signal for producing an optical pulse at the portion where the optical code was converted. The so-adjusted pulse signal is combined with the IP packet in the combiner 82 and the combined signal is directed into a nonlinear optical medium 85 to produce a cross-phase modulation effect. A semiconductor optical amplifier, optical fiber or the like can be used as the nonlinear optical medium.

The pulse consisting of the IP packet overlaid on the control light is subjected to phase modulation satisfying the relationship represented by the following Equation (1). The desired phase modulation can therefore be achieved by regulating the power and

wavelength of the control light, the length of the nonlinear optical medium and the like.

$$\phi_{\max}(t) = \frac{4\pi n_2}{\lambda_s A_{\text{eff}}} \int_0^L P(t - \tau z) dz \quad \dots\dots\dots (1)$$

where

$$\tau = \frac{1}{V_c} - \frac{1}{V_s}$$

$\phi_{\max}(t)$ : phase modulation amount

$\lambda_s$ : (nm): signal light wavelength

$n_2$  (1/W/m<sup>2</sup>): nonlinearity coefficient

$A_{\text{eff}}$  (m<sup>2</sup>): effective area

$P(t)$ : control light wavelength

$\tau$  (s/m): propagation difference of control light and signal light per unit

length

$L(m)$  : length of nonlinear optical medium

$V_c$  (m/s): signal light speed

$V_s$  (m/s): control light speed

An IP packet with a converted optical code is obtained by passing the phase-modulated IP packet through an optical filter optical filter 86.

FIG. 11(b) is a diagram illustrating the operating principle of optical code conversion conducted by the optical code converter of FIG. 11(a).

The optical encoder 84 is adjusted to produce control pulses at a portion corresponding to two pulses of the latter half desired to be converted. The optical signal of this control pulse is combined with the optical signal to be modulated in the combiner 82 and is converted into a  $[00 \phi_{\max} \phi_{\max}]$  optical code by interaction in the nonlinear optical medium 85. Utilizing the relationship represented by Equation (1), the control light power, wavelength, length of the nonlinear optical medium and the like are adjusted to make  $\phi_{\max}$  equal to  $\pi$ . By this, the  $[0000]$  optical code can be converted to a  $[00\pi \pi]$  optical code.

The foregoing also essentially applies when the optical code of the IP packet is a "0", "1" intensity code, i.e., the optical encoder 84 is adjusted to produce control light having a control pulse  $[0011]$  at a portion corresponding to two pulses of the latter half desired to be converted.

When an IP packet having a code  $[1111]$  is directed into a loop mirror 82' (FIG. 12) serving as a combiner, it is divided into clockwise and counterclockwise

components. The control light sent from the optical encoder 84 proceeds to the left in the loop mirror 82' and combines with the optical code of the IP packet. The optical code traveling in the same direction as the control light has its phase changed to  $\phi_{\max}$  by interaction in the nonlinear optical medium. Utilizing the relationship represented by Equation (1), the control light power, wavelength, length of the nonlinear optical medium and the like are adjusted to make  $\phi_{\max}$  equal to  $\pi$ . By this, the [1111] optical code can be converted to a [1100] optical code.

Since this invention thus enables all aspects of IP packet code conversion to be conducted optically, it is capable of achieving optical code conversion of terabit-class high-speed pulse trains.

As explained in the foregoing, the photonic network packet routing method according to the present invention labels packets with destination address information encoded using light attributes and, at every node of the photonic network receiving the packets, sends the address information of each packet onto a number of arms equal to the number of output paths, conducts parallel discrimination by optical correlation processing simultaneously on all arms, and switches the packet to an output path based on the result of the discrimination. Packet routing can therefore be conducted at higher speed than by the conventional packet routing method in which switching to the output paths is done in accordance with address information after conversion from optical to electric address information. In particular, since the packet routing method of the present invention enables application of photonic technology up to the point of transmission function, it can be expected to be widely utilized as a fundamental technology for realizing an ultra-high speed, high-functionality photonic network.

In the packet router for a photonic network according to the present invention, each packet labeled with optically encoded destination address information using light attributes is sent by branching means over two separate paths to an address control means and a switch means, the address control means conducts optical correlation processing to discriminate the address information of the one packet received thereby from the branching means and outputs an address control signal based on the result of the discrimination, and the switching means switches the output path of the other packet received thereby from the branching means based on the address control signal from the address control means. Packet routing can therefore be conducted at higher speed than by the conventional packet router that switches output paths in accordance with address information after conversion from optical to electric address information.

The present invention further enables conversion of IP packet optical codes to be conducted optically.



An ultra-high speed, high-functionality photonic network can therefore be realized by using the packet router at every node of a photonic network.

What is claimed is:

1. A photonic network packet routing method comprising:  
a step of optically encoding destination address information attached to an IP  
packet using light attributes,

5 a step of discriminating the encoded address information of the IP packet by  
optical correlation processing,

a step of switching to an output path for the IP packet based on a result of the  
discrimination, and

10 a step of outputting the IP packet labeled with prescribed address information  
on the output path selected by the switching step.

2. A packet routing method according to claim 1, wherein the optical encoding  
of the destination address information attached to the IP packet is conducted by dividing  
an optical pulse output by a pulse source into N number of chip pulses ( $N \geq 2$ ) having a  
prescribed delay time therebetween, imparting the individual chip pulses with phase  
15 shifts of "0" or " $\pi$ " relative to a light carrier phase of the chip pulses, and recombining  
the divided optical chip pulses.

3. A packet routing method according to claim 1, wherein the optical encoding  
of the destination address information attached to the IP packet is conducted by dividing  
an optical pulse output by a pulse source into N number of chip pulses ( $N \geq 2$ ) having a  
prescribed delay time therebetween, changing normalized intensity of the individual chip  
20 pulses to "1" or "0", and recombining the divided optical chip pulses.

4. A packet routing method according to claim 1, wherein discrimination of the  
optically encoded address information is conducted by sending the IP packet labeled  
with address information onto a number of arms equal to the number of address entries  
25 and simultaneously conducting optical correlation processing on all arms in parallel.

5. A packet routing method according to claim 1, wherein discrimination of the  
encoded address information by optical correlation processing is conducted by subjecting  
optical chip pulses to matched filtering, effecting threshold processing on a center peak  
value of a generated autocorrelation function, and optically regenerating the obtained "0"  
30 or "1".

6. A packet routing method according to claim 1, further comprising a step of  
subjecting an output of an optical decoder to time gate processing, when subjecting a  
center peak value of a correlation function to threshold processing, thereby cutting off a  
center part and eliminating side-lobes of correlation waveform and a step of conducting  
35 threshold processing.

7. A packet routing method according to claim 1, further comprising a step of

dividing an IP packet having encoded address information in two, a step of conducting optical correlation processing to discriminate address information from an optical code in one IP packet containing address information between the two divided IP packets, a step of selecting an output path based on a result of the discrimination, and a step of outputting the other divided IP packet on the selected output path.

8. A packet routing method according to claim 6, wherein the address information is discriminated by sending the one IP packet onto a number of arms equal to the number of output paths and simultaneously conducting optical correlation processing on all arms in parallel.

9. A packet routing method according to claim 7, wherein an optical code in the one packet is discriminated by optical correlation processing, the discriminated signal is converted to an electric signal, and a gate of a prescribed output path is opened by the electric signal.

10. A packet routing method according to claim 7, wherein an optical code in the one packet is discriminated by optical correlation processing and an optical switch of a prescribed output path is turned ON by the discriminated optical signal.

11. A packet routing method according to claim 1, further comprising a step of combining the IP packet output on the prescribed path and a pulse signal for control adjusted to generate an optical pulse at a portion where it is desired to convert the optical code and a step of converting the combined signal into a prescribed optical code by cross-phase conversion.

12. A packet router for a photonic network comprising:

means for encoding by use of light attributes destination address information attached to an IP packet,

branching means for sending the IP packet having the encoded destination address information onto two paths,

address processing means for subjecting one IP packet received from the branching means to optical correlation processing and outputting a switch control signal based on a result of the discrimination, and

switch means for selectively outputting the packet by switching an output path of the other packet received from the branching means based on the address control signal from the address processing means.

13. A packet router according to claim 11, wherein the encoding means comprises multiple tunable taps for dividing a light pulse output by a pulse source into a prescribed number of optical chip pulses, optical phase shifters for imparting phase shifts of "0" or " $\pi$ " to each divided chip pulse, and a combiner for recombining the divided

optical chip pulses.

14. A packet router according to claim 11, wherein the encoding means comprises multiple tunable taps for dividing a light pulse output by a pulse source into a prescribed number of optical chip pulses, gate switches for changing optical intensity of the chip pulses to "1" or "0", and a combiner for recombining the divided optical chip pulses.

15. A packet router according to claim 11, wherein the address processing means comprises means for sending the one IP packet sent onto one path by the branching means onto a number of arms equal to the number of addresses, and a decoder provided on the individual arms for outputting a switch control signal when the decoder's own code and the code of IP packet coincide.

16. A packet router according to claim 14, wherein the switch means comprises means for sending the other IP packet sent onto the other path by the branching means onto a number of arms equal to the number of output ports and an optical gate provided on each arm that opens in response to a switching control signal from the decoder to output the IP packet onto the arm.

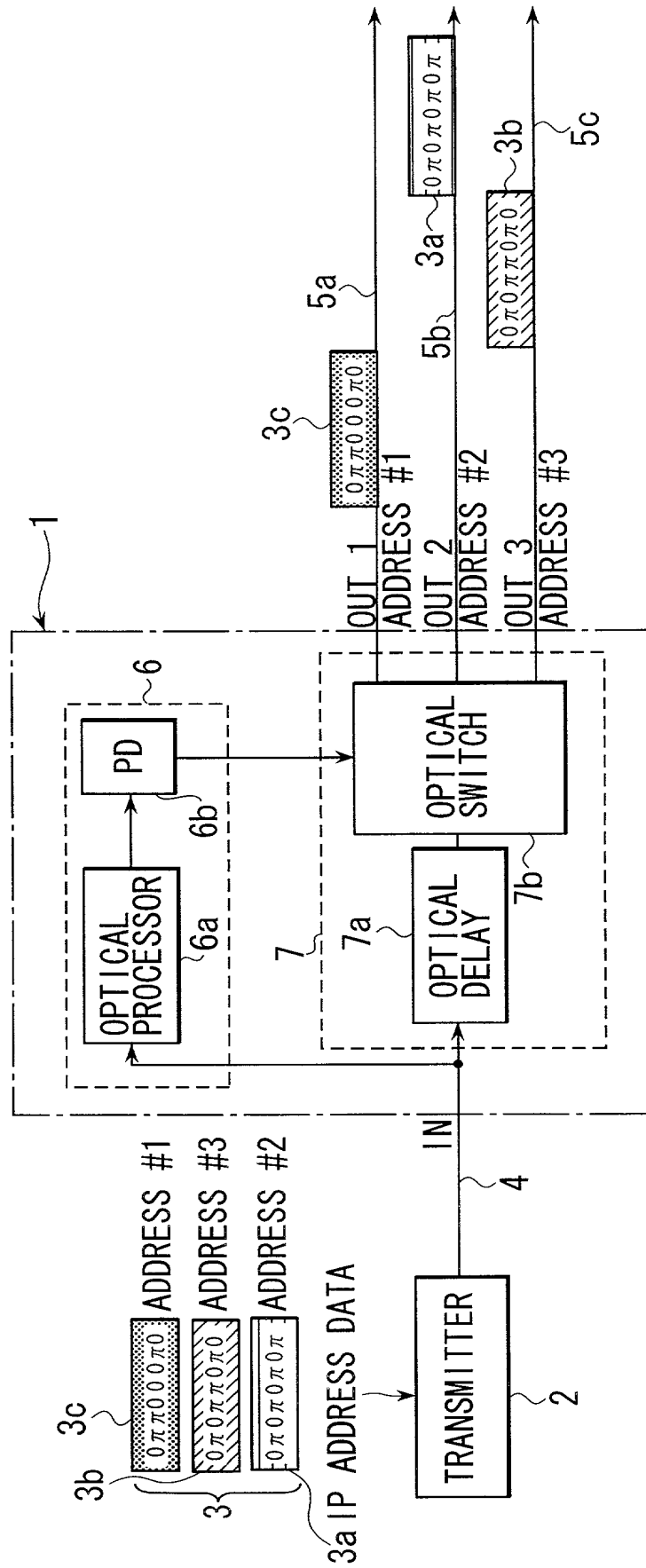
17. A packet router according to claim 11, further comprising a combiner for combining an IP packet output through a prescribed path with a pulse signal for control adjusted to generate an optical pulse at a portion where it is desired to convert the optical code and a nonlinear optical medium for converting the combined signal into a prescribed optical code by cross-phase conversion.

## ABSTRACT OF THE DISCLOSURE

A photonic network packet routing method includes the steps of optically encoding destination address information attached to an IP packet using light attributes, discriminating the encoded address information of the IP packet by optical correlation processing, switching to an output path for the IP packet based on a result of the discrimination, and outputting the IP packet labeled with prescribed address information on the output path selected by the switching step. A packet router for a photonic network includes a device for encoding by use of light attributes destination address information attached to an IP packet, a branching device for sending the IP packet having the encoded destination address information onto two paths, an address processing device for subjecting one IP packet received from the branching device to optical correlation processing and outputting a switch control signal based on a result of the discrimination, and a switch device for selectively outputting the packet by switching an output path of the other packet received from the branching device based on the address control signal from the address control device.

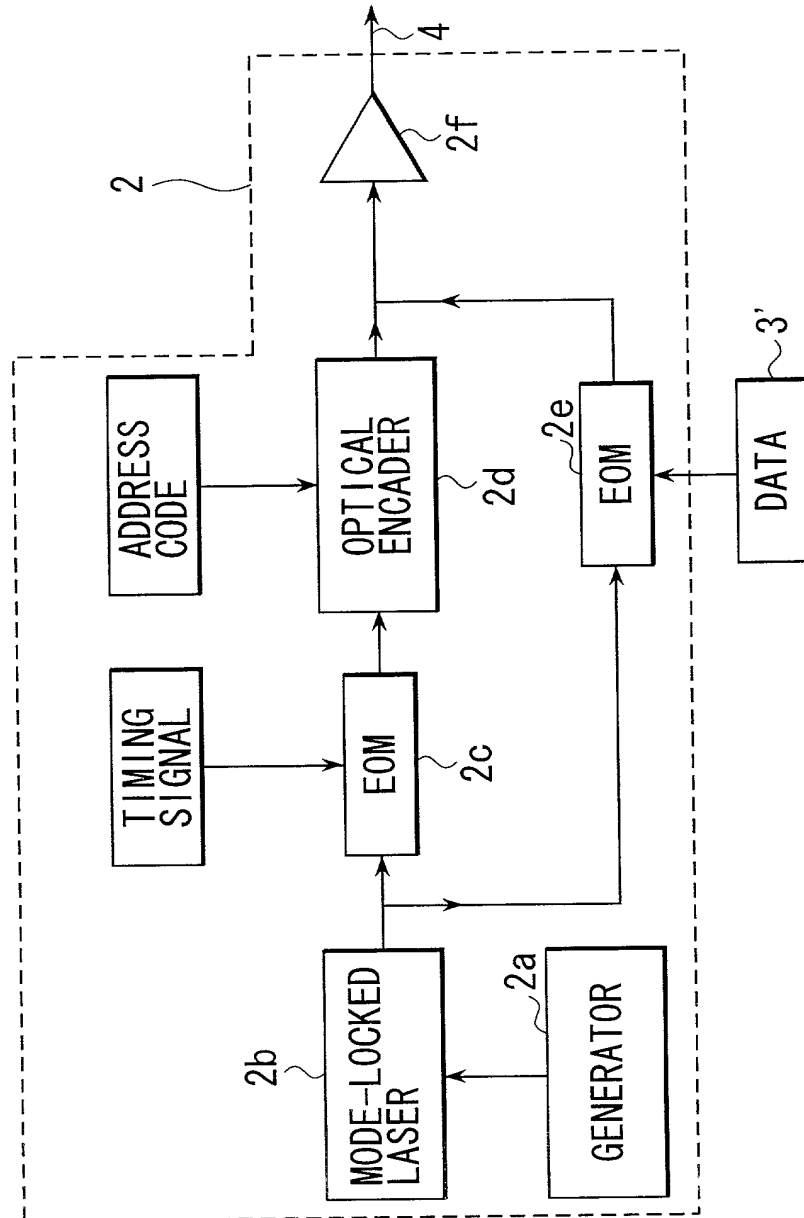
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FIG. 1



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FIG. 2



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FIG. 3 (a)

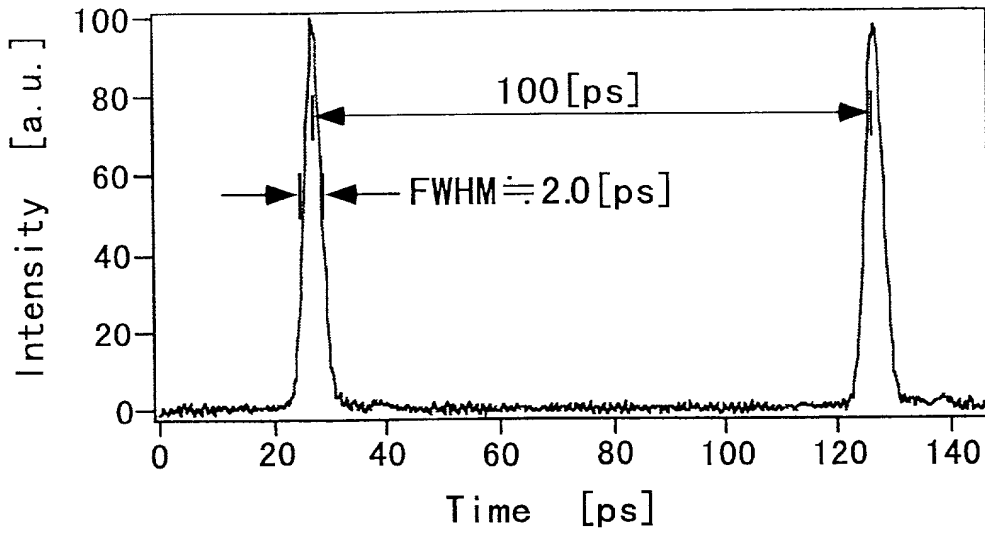


FIG. 3 (b)

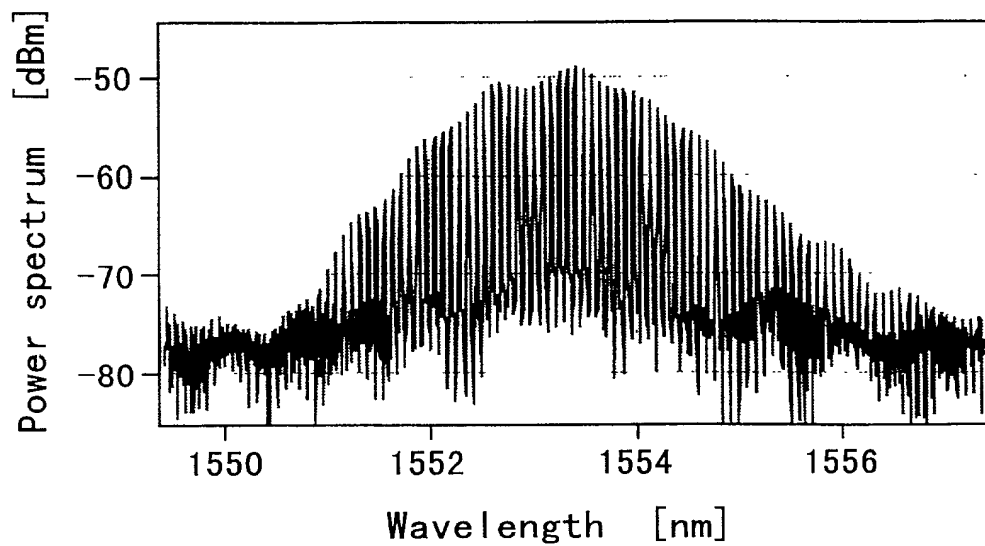




FIG. 4

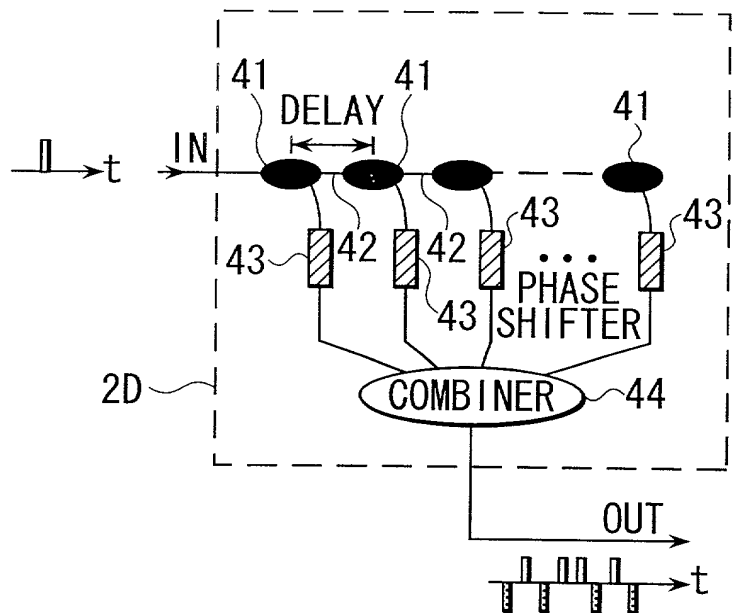
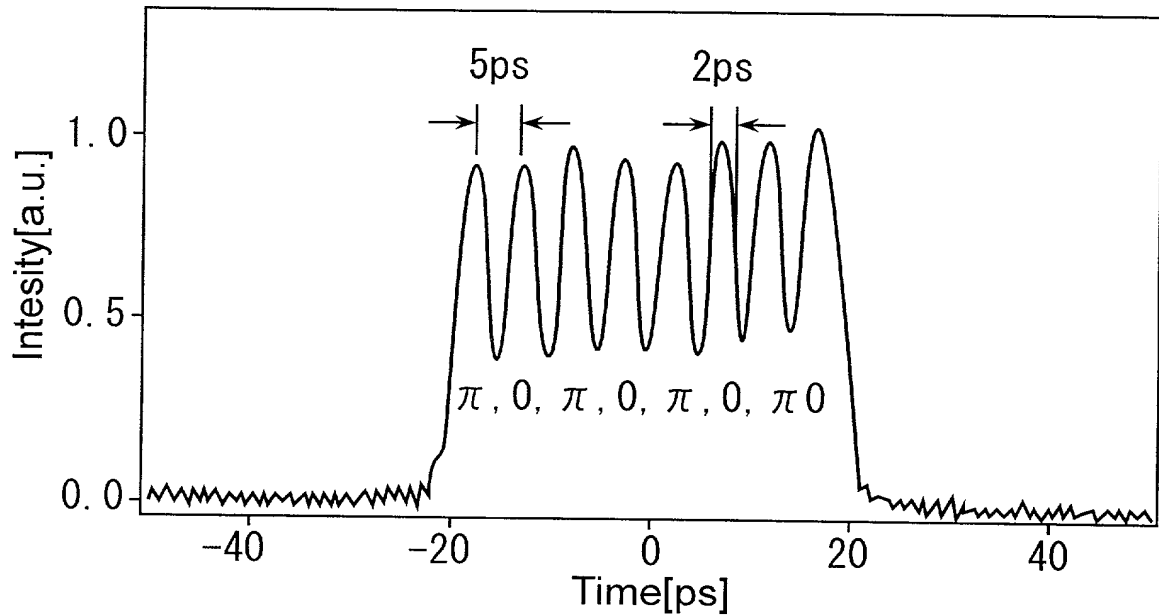
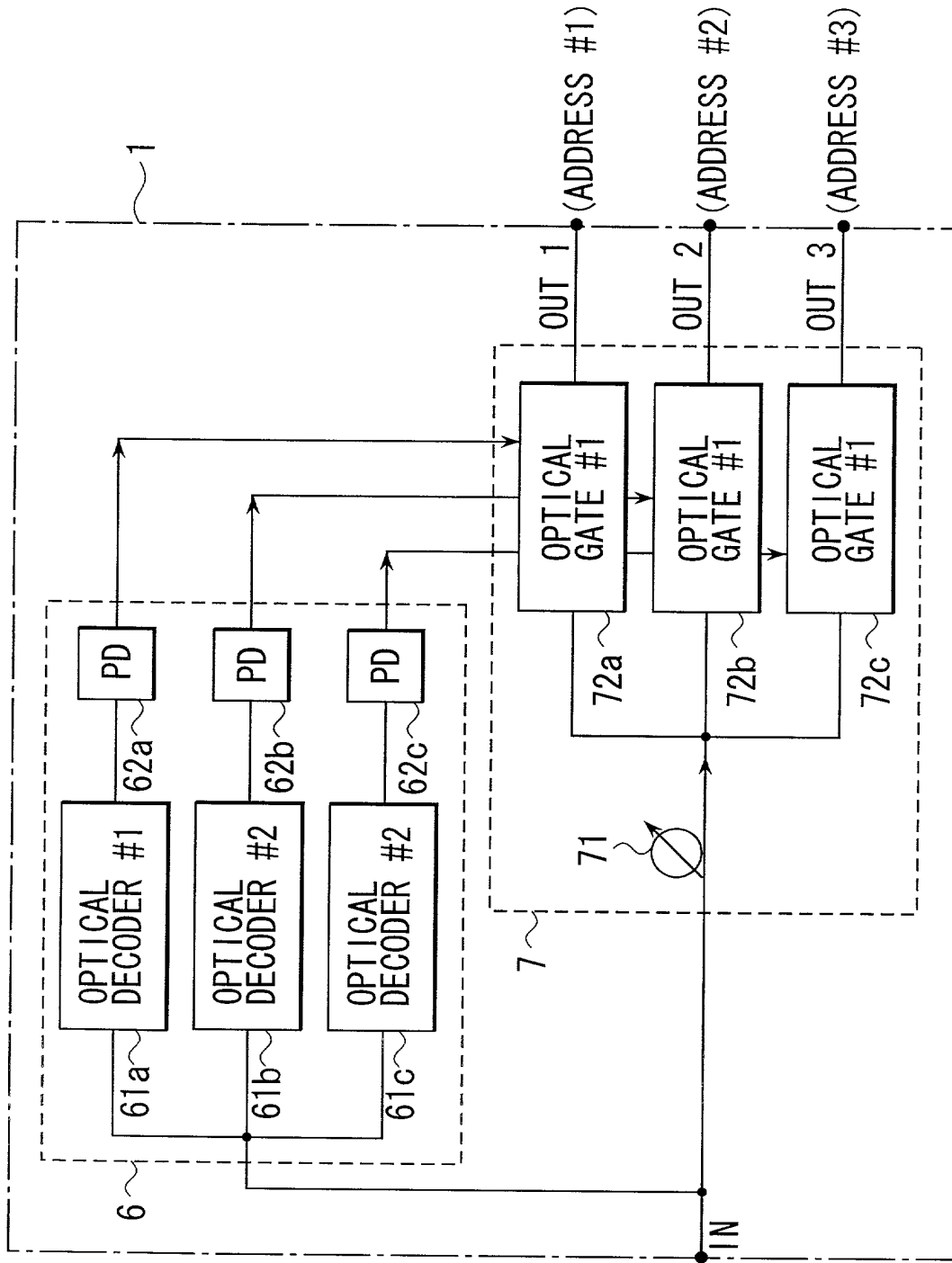


FIG. 5



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FIG. 6



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FIG. 7 (a)

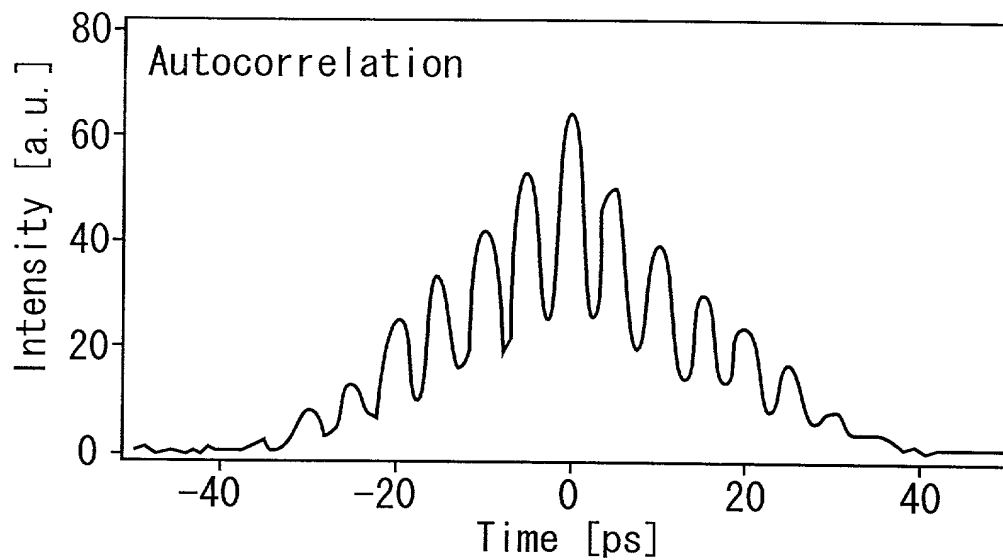
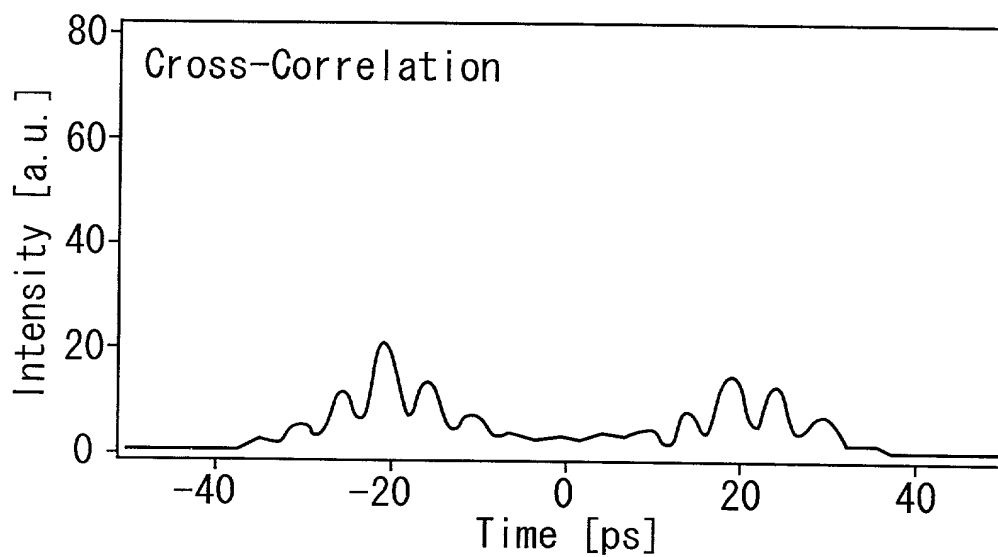
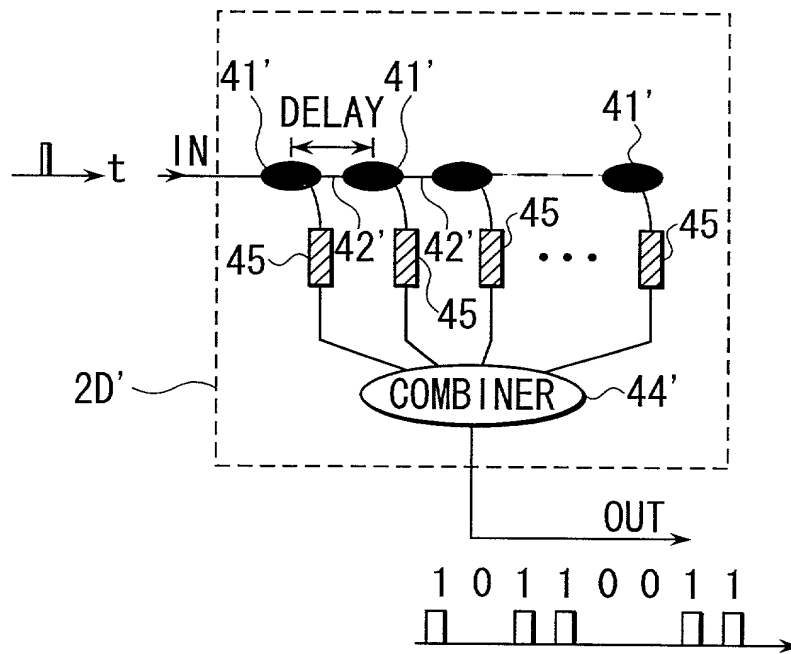


FIG. 7 (b)

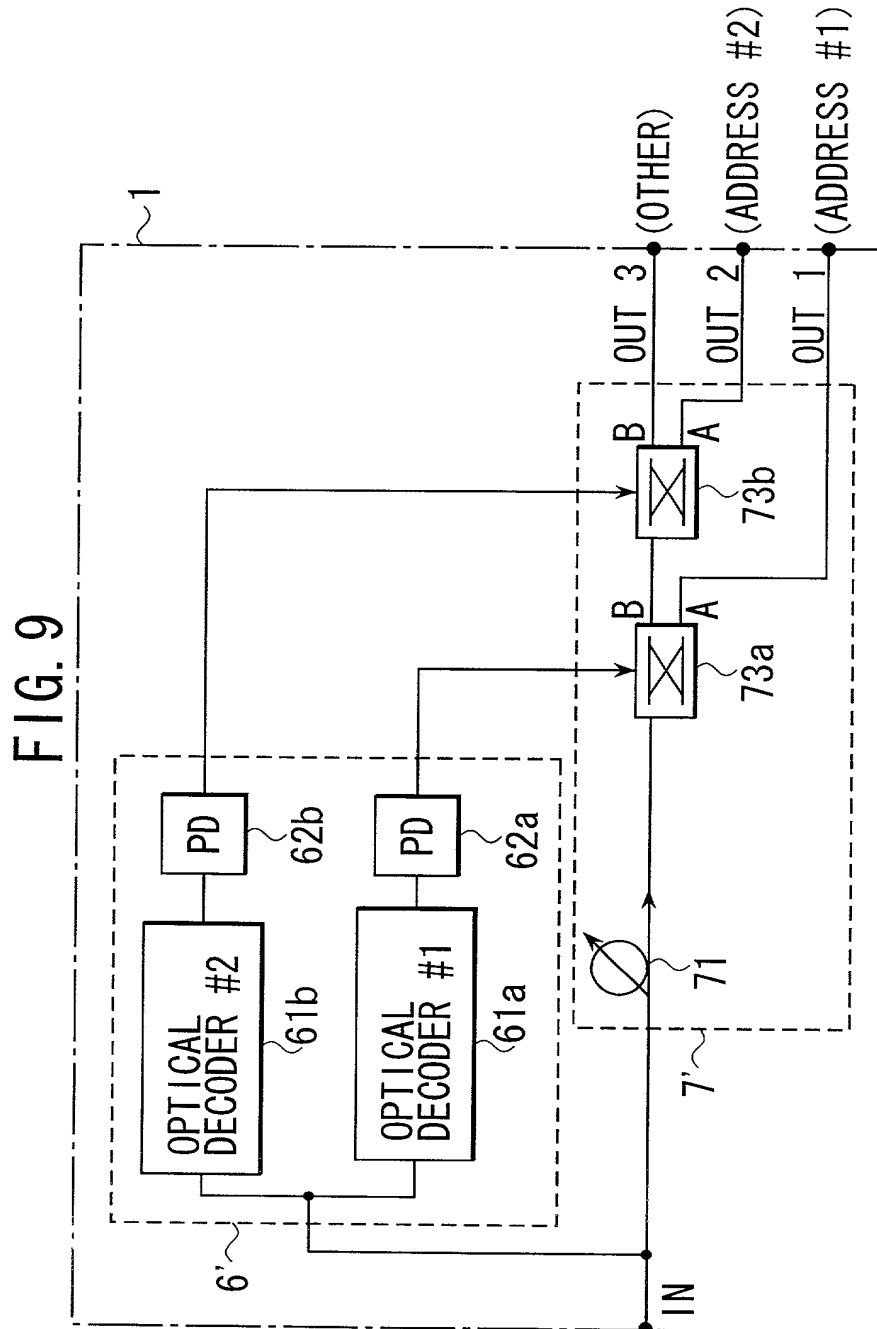


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FIG. 8



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FIG. 10

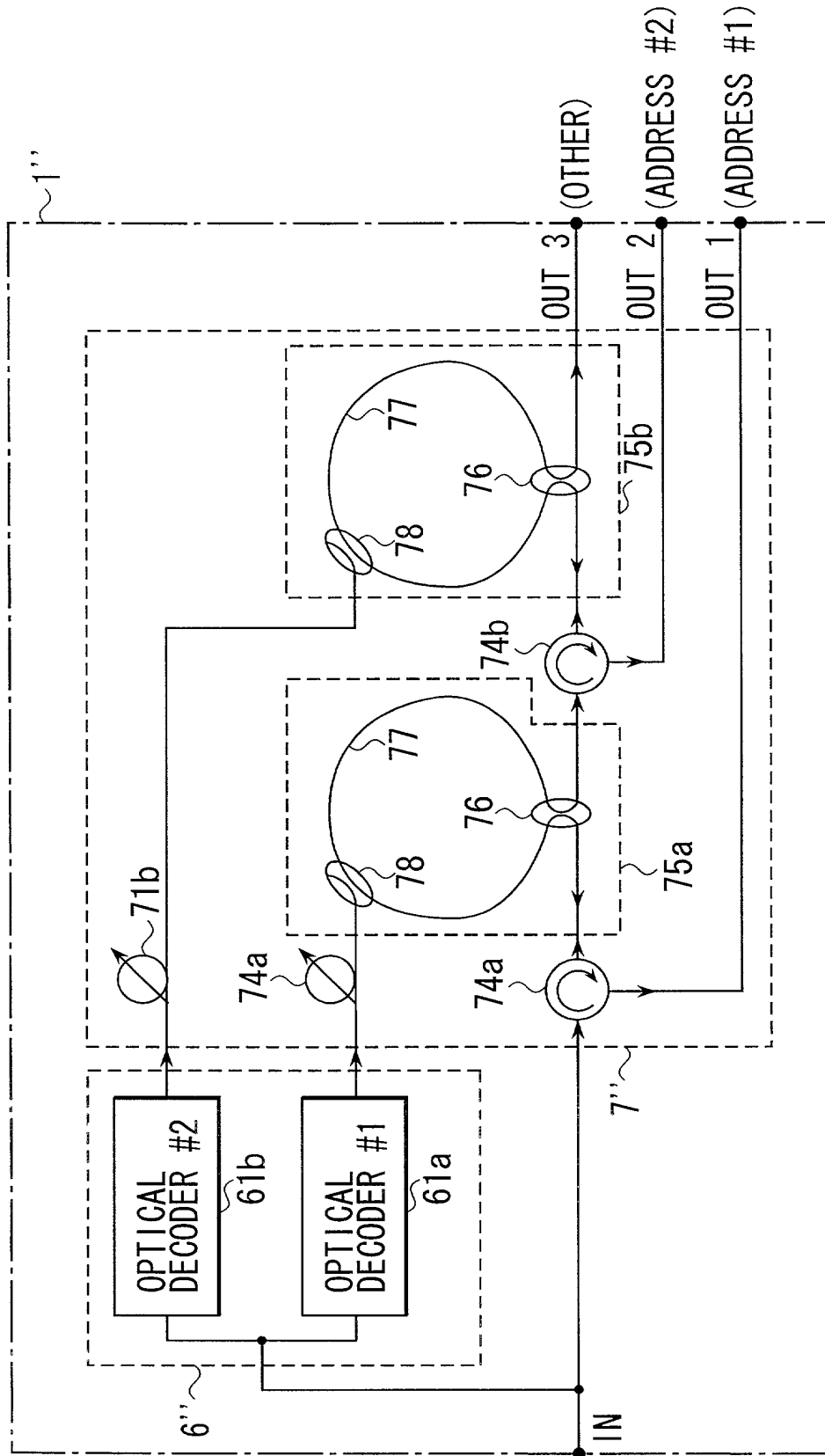


FIG. 11 (a)

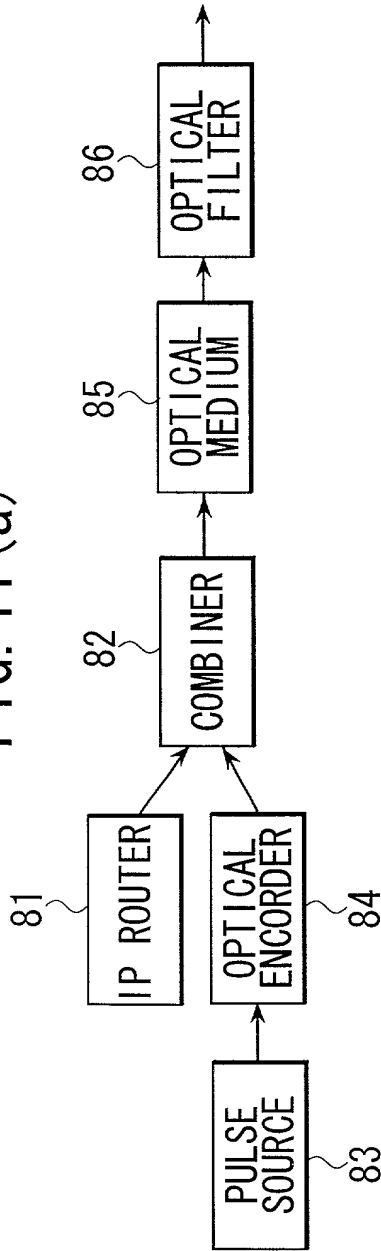
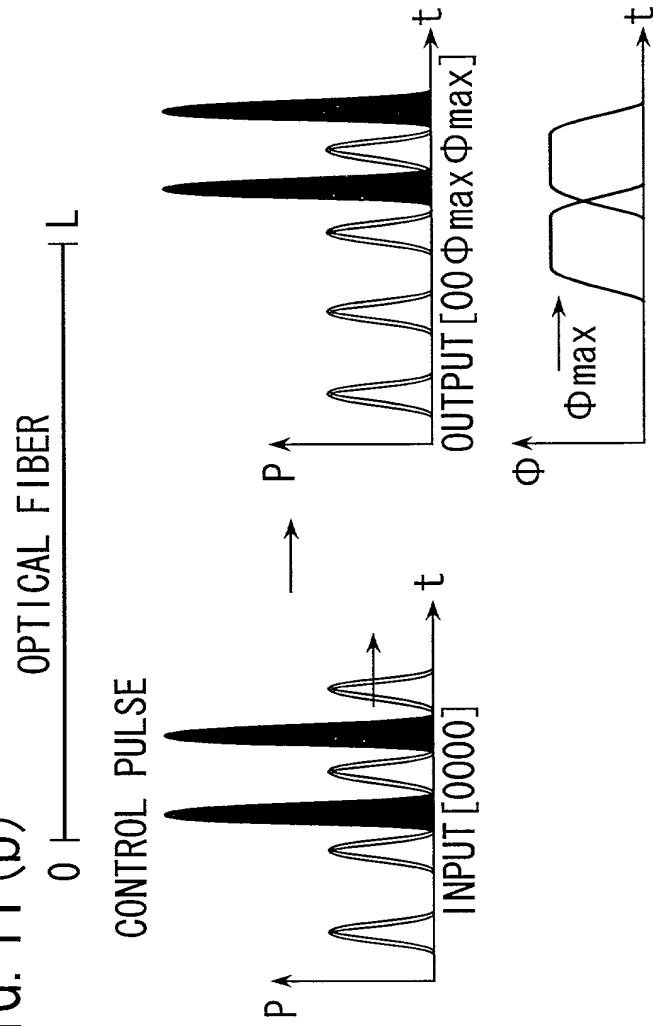


FIG. 11 (b)



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FIG. 12

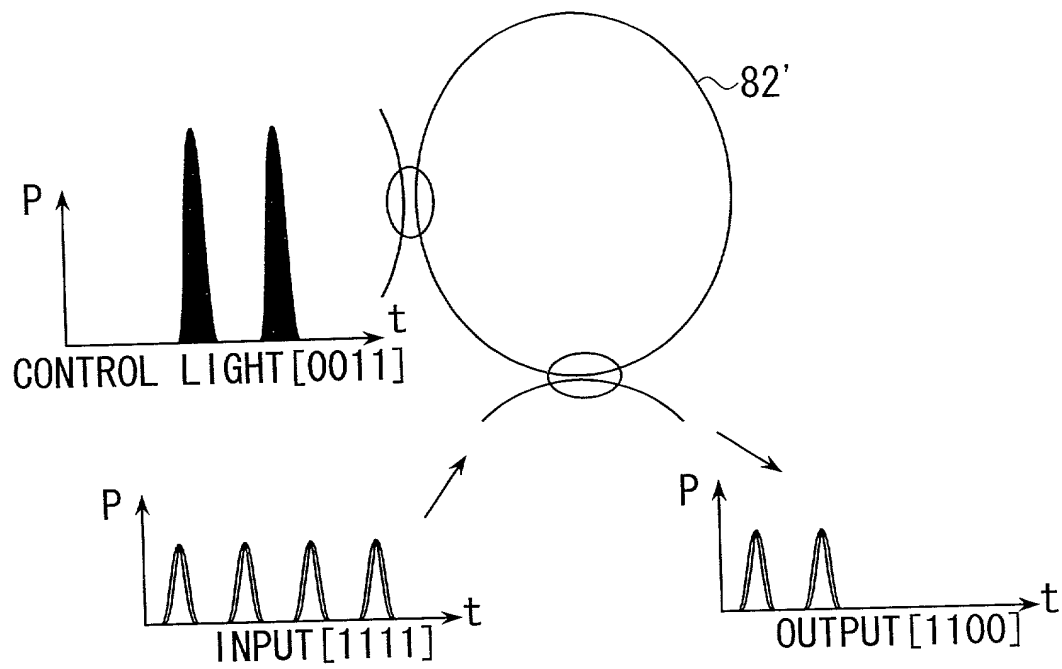
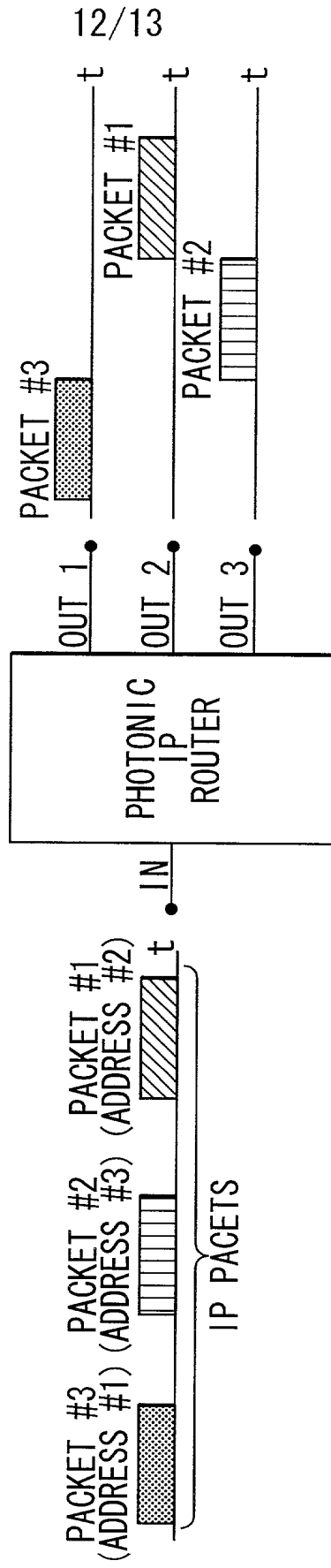


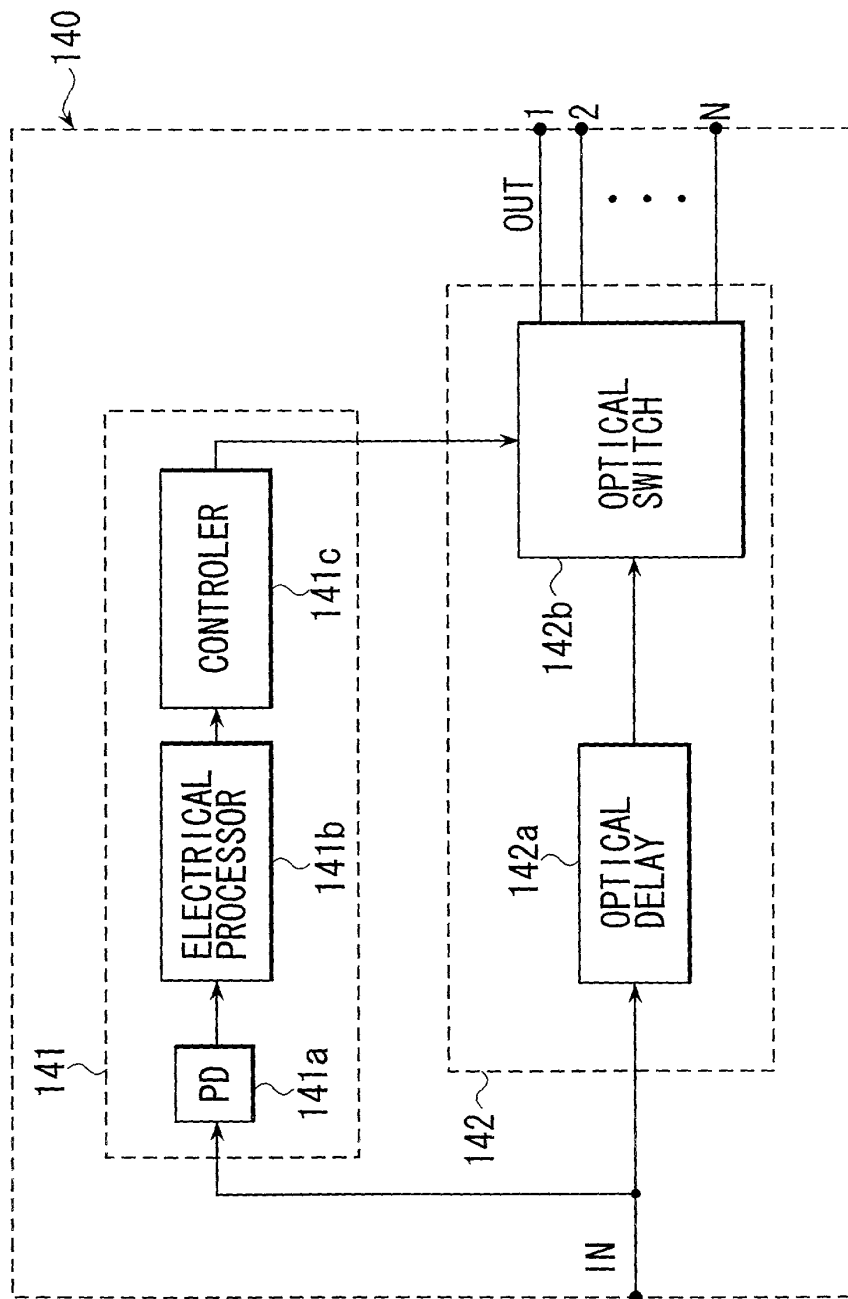


FIG. 13 PRIOR ART



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FIG. 14 PRIOR ART



# Declaration and Power of Attorney For Patent Application

## 特許出願宣言書及び委任状

### Japanese Language Declaration

#### 日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者（下記の名称が複数の場合）であると信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled.

Photonic network packet routing  
method and packet router for  
photonic network

上記発明の明細書は、

the specification of which

☐ 本書に添付されています。

☒ is attached hereto.

☐ \_\_\_\_月\_\_\_\_日に提出され、米国出願番号または特許協定条約国際出願番号を\_\_\_\_とし、  
(該当する場合) \_\_\_\_に訂正されました。

☐ was filed on \_\_\_\_  
as United States Application Number or  
PCT International Application Number  
\_\_\_\_ and was amended on  
\_\_\_\_ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

# Japanese Language Declaration

(日本語宣言書)

私は、米国法典第35編119条 (a) - (d) 項又は365条 (b) 項に基づき下記の、米国以外の国の少なくとも一カ国を指定している特許協力条約365 (a) 項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)

外国での先行出願

11-355967

(Number)  
(番号)

JAPAN

(Country)  
(国名)

(Number)  
(番号)

(Country)  
(国名)

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(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

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(出願番号)

(Filing Date)  
(出願日)

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

私は、私自信の知識に基づいて本宣言書中で私が行なう表明が真実であり、かつ私の入手した情報と私の信じているところに基づく表明が全て真実であると信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編第1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の声明を行なえば、出願した、又は既に許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Claimed  
優先権主張

15/December/1999

(Day/Month/Year Filed)  
(出願年月日)

☒

Yes  
はい

☐

No  
いいえ

☐

Yes  
はい

☐

No  
いいえ

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

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(Status: Patented, Pending, Abandoned)  
(現況: 特許許可済、係属中、放棄済)

(Status: Patented, Pending, Abandoned)  
(現況: 特許許可済、係属中、放棄済)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

# Japanese Language Declaration

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(弁護士、または代理人の指名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: (list name and registration number)

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国籍	Citizenship JAPAN
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(第三以降の共同発明者についても同様に記載し、署名すること)

(Supply similar information and signature for third and subsequent joint inventors.)

# Japanese Language Declaration

(日本語宣言書)

第三の共同発明者の氏名	Full name of third joint inventor, if any Ken-ichi KITAYAMA
第三の共同発明者の署名	Third joint Inventor's signature  Date June 7, 2000
住所	Residence Suita-shi, Osaka, JAPAN
国籍	Citizenship JAPAN
郵便の宛先	Post Office Address c/o Department of Electronics and Information Systems, Osaka University
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第四の共同発明者の氏名	Full name of fourth joint inventor, if any
第四の共同発明者の署名	Fourth joint Inventor's signature
住所	Residence
国籍	Citizenship
郵便の宛先	Post Office Address

第五の共同発明者の氏名	Full name of fifth joint inventor, if any
第五の共同発明者の署名	Fifth joint Inventor's signature
住所	Residence
国籍	Citizenship
郵便の宛先	Post Office Address

第六の共同発明者の氏名	Full name of sixth joint inventor, if any
第六の共同発明者の署名	Sixth joint Inventor's signature
住所	Residence
国籍	Citizenship
郵便の宛先	Post Office Address

(第六またはそれ以降の共同発明者に対しても同様な情報および署名を提供すること。)

(Supply similar information and signature for third and subsequent joint inventors.)